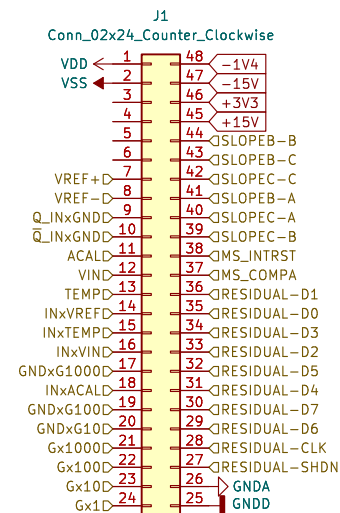
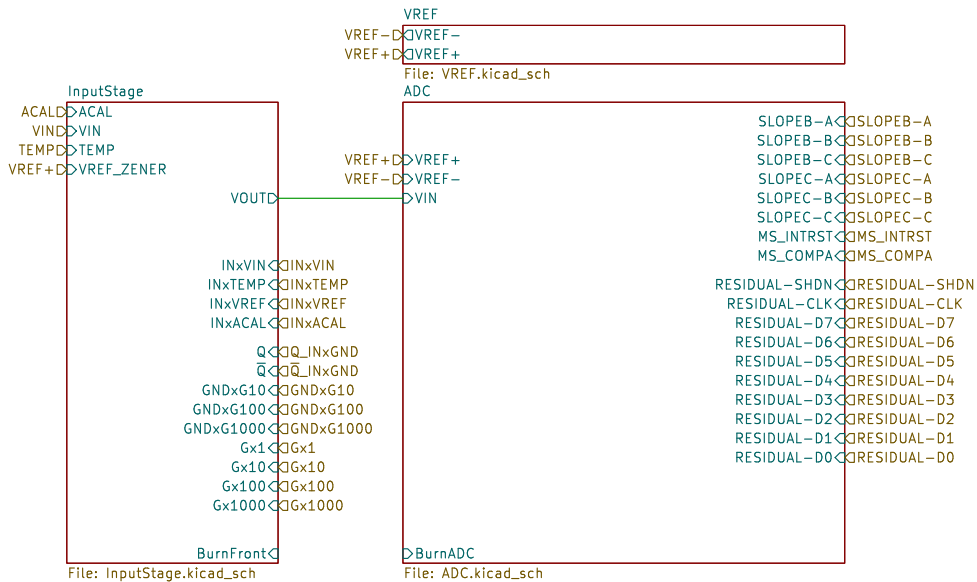




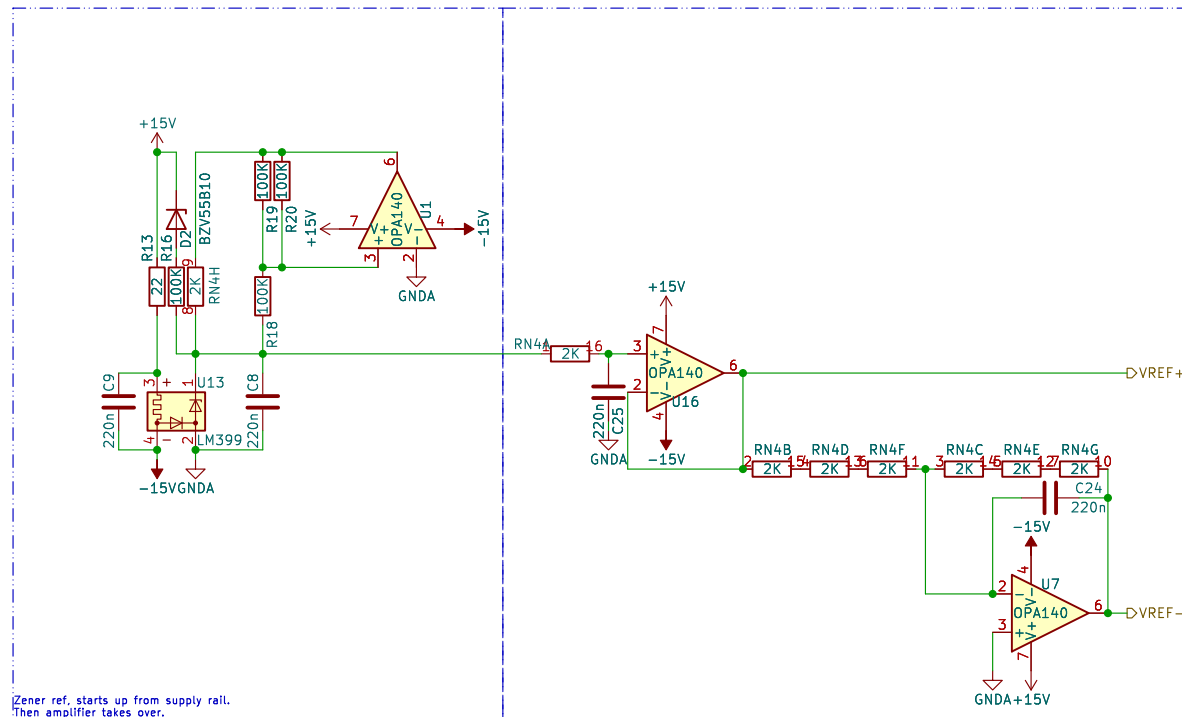
Sheet: /
File: xMultislope-1v0.kicad_sch

Title: xMultislope-1v0

Size: A4 Date: 2023-11-01

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Rev:
Id: 1/4



Goal of this block is to generate a low-noise, low-drift and stable voltage reference.
Voltage reference should output +12V and -12V, the exact value not really critical.
Besides this a raw, ultra stable output should also be provided. Reference should generate it's own supply.

Sheet: /VREF/
File: VREF.kicad_sch

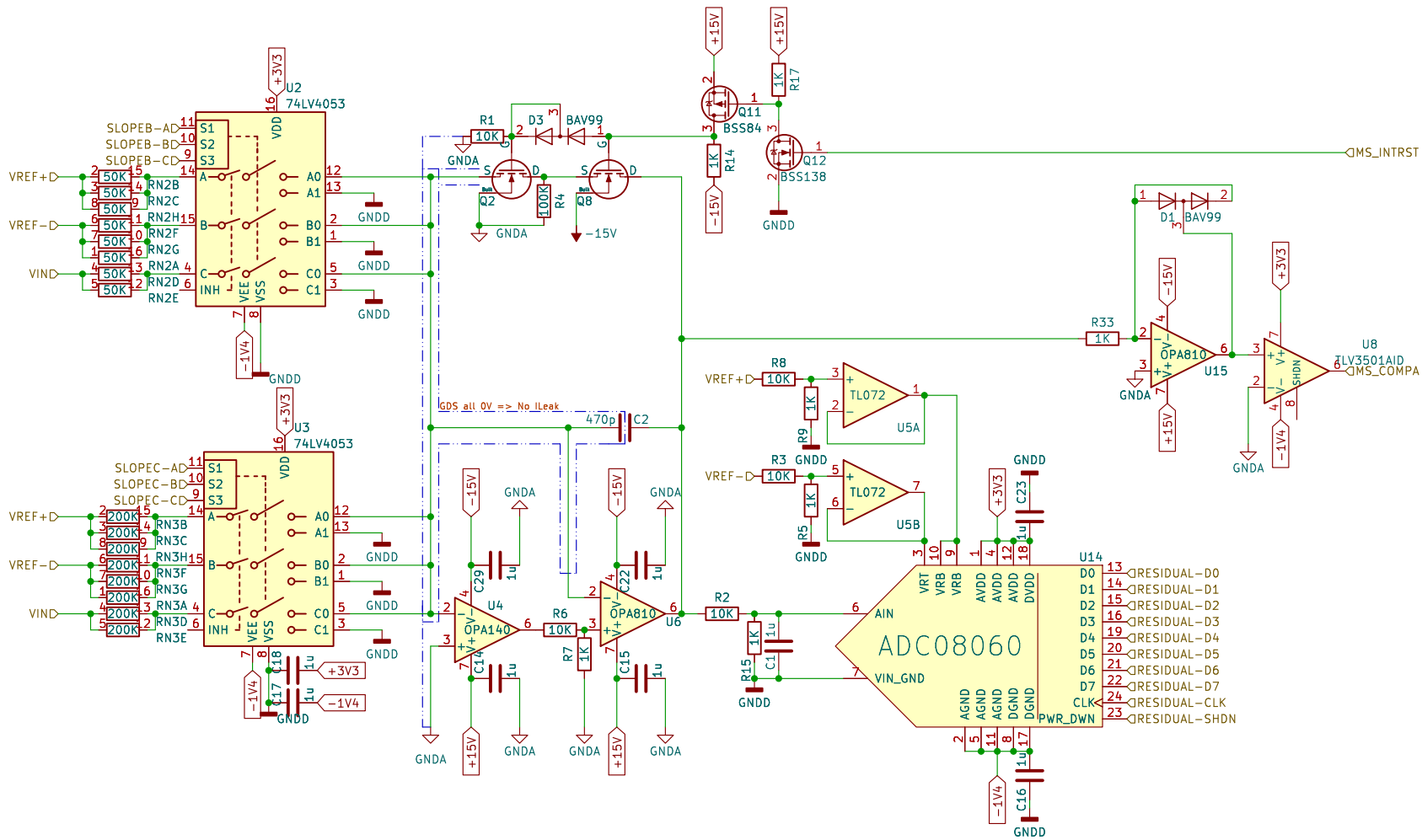
Title: xMultislope-1v0

Size: A4 Date: 2023-11-01

KiCad E.D.A. 8.0.8

Rev:

Id: 2/4



This is the main ADC block. Here a A/D conversion occurs by means of charge balancing.

Sheet: /ADC/
File: ADC.kicad_sch

Title: xMultislope-1v0

Size: A4 Date: 2023-11-01

KiCad E.D.A. 8.0.8

Rev:

Id: 4/4